

Fig. 1

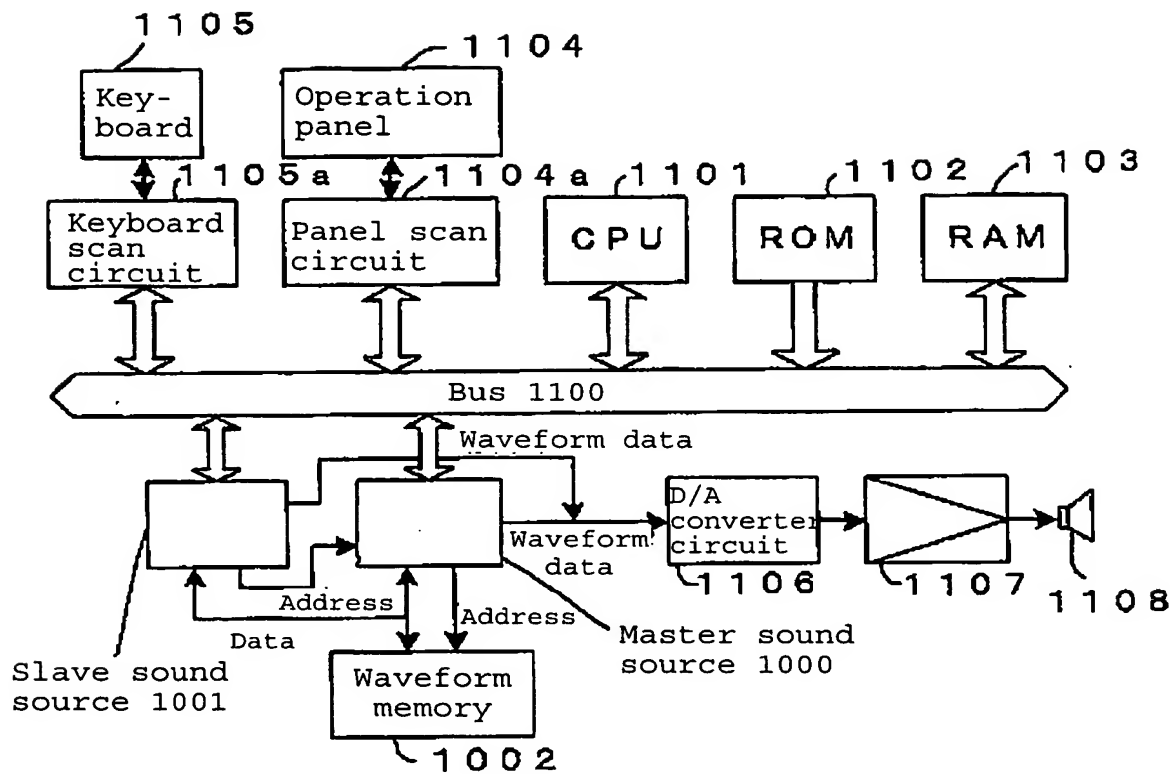


Fig. 2

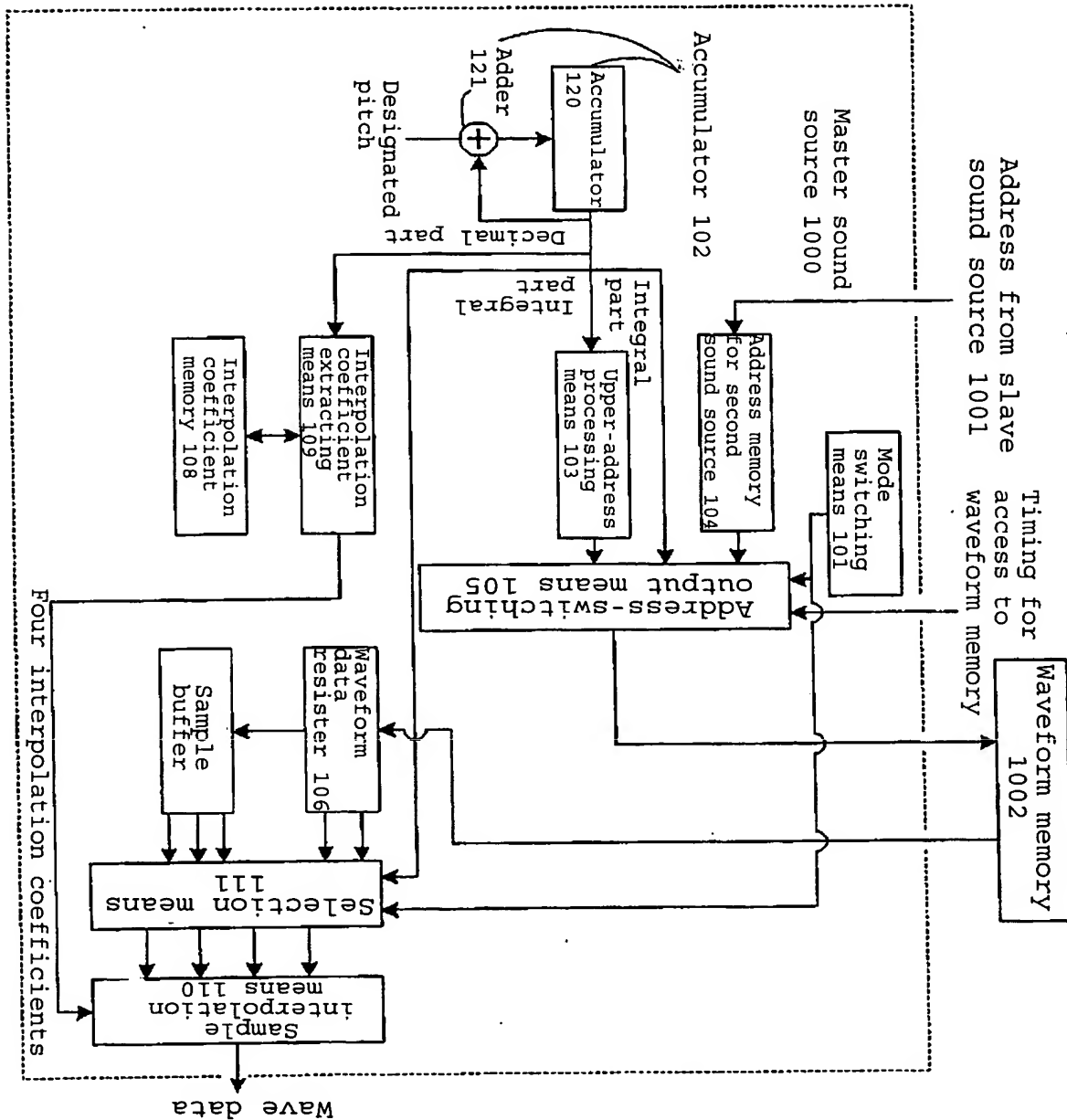


Fig. 3

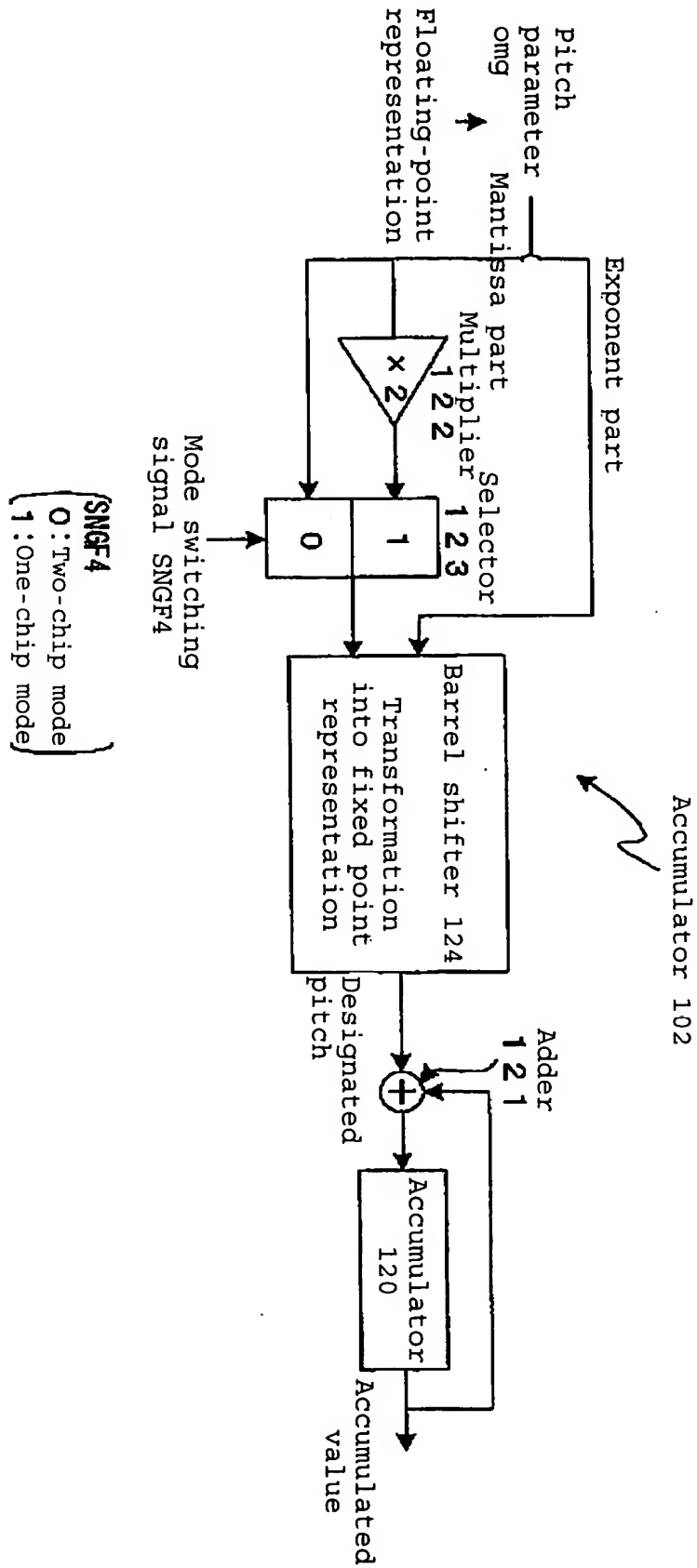


Fig. 4

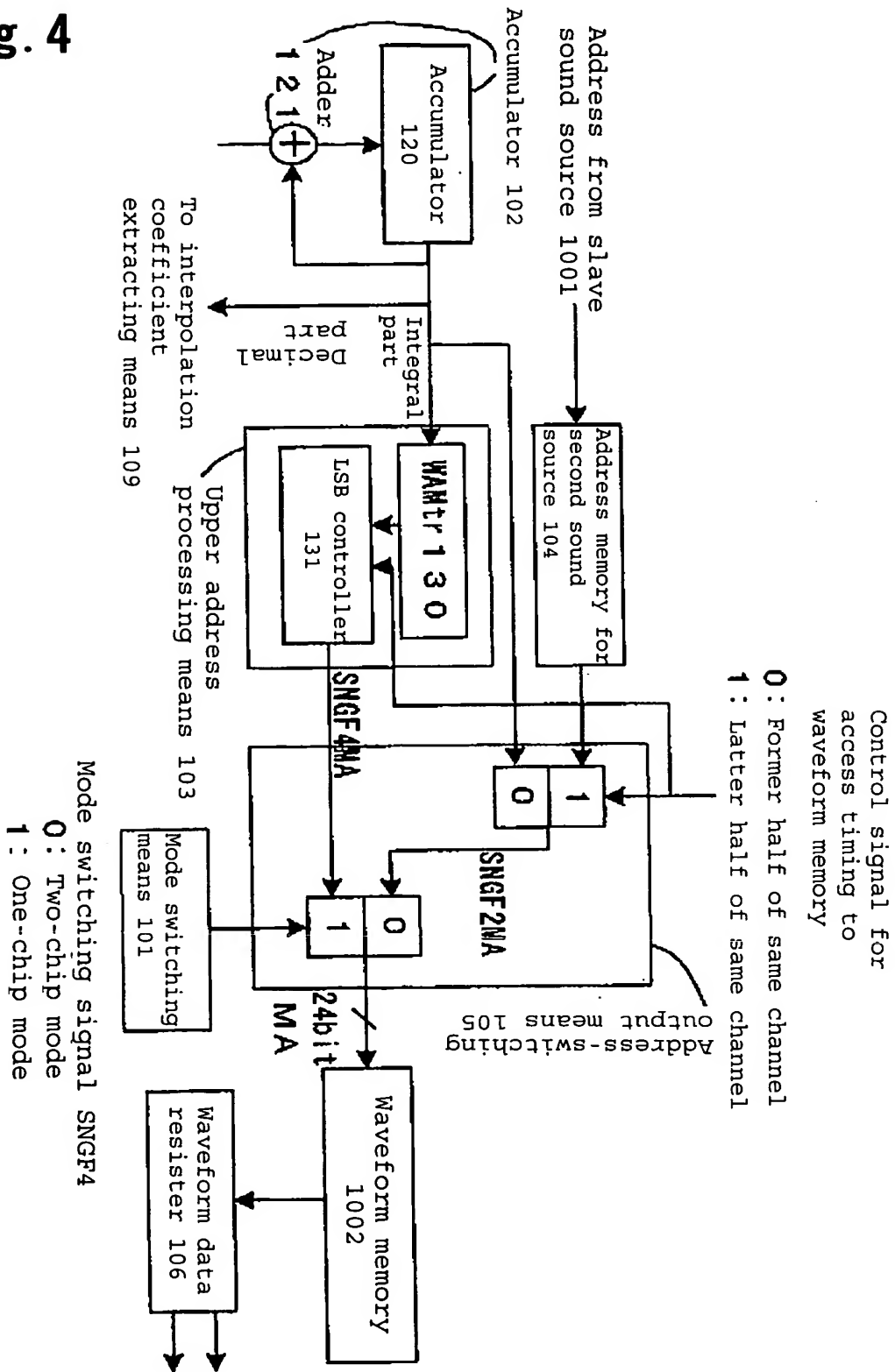


Fig. 5

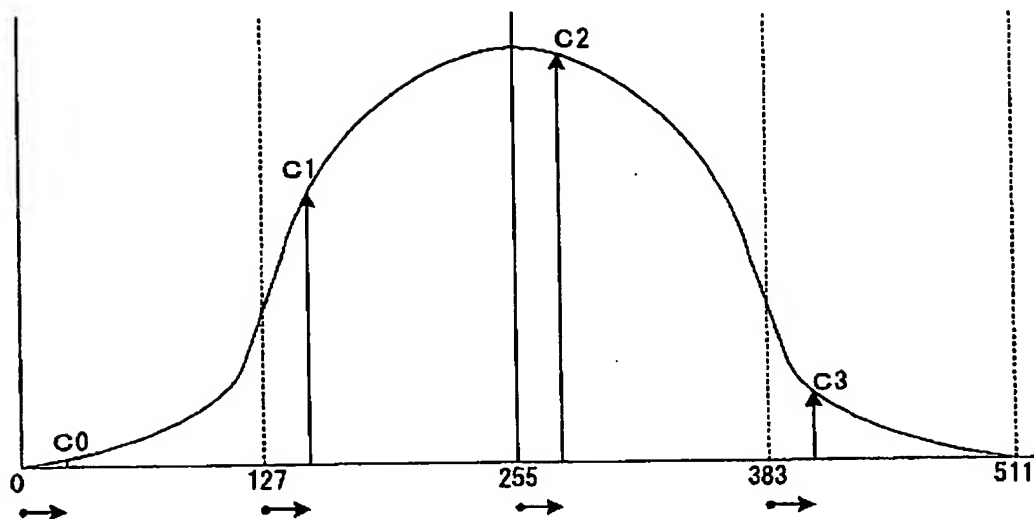
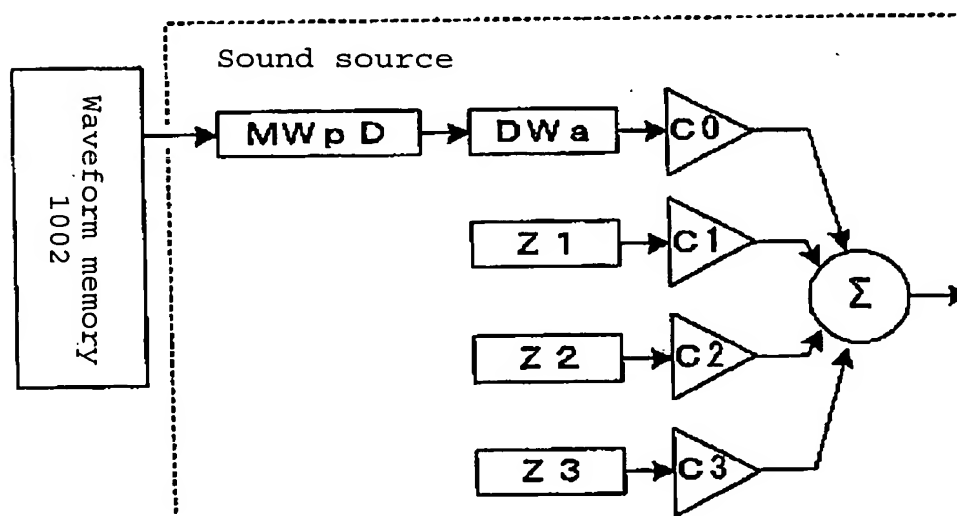
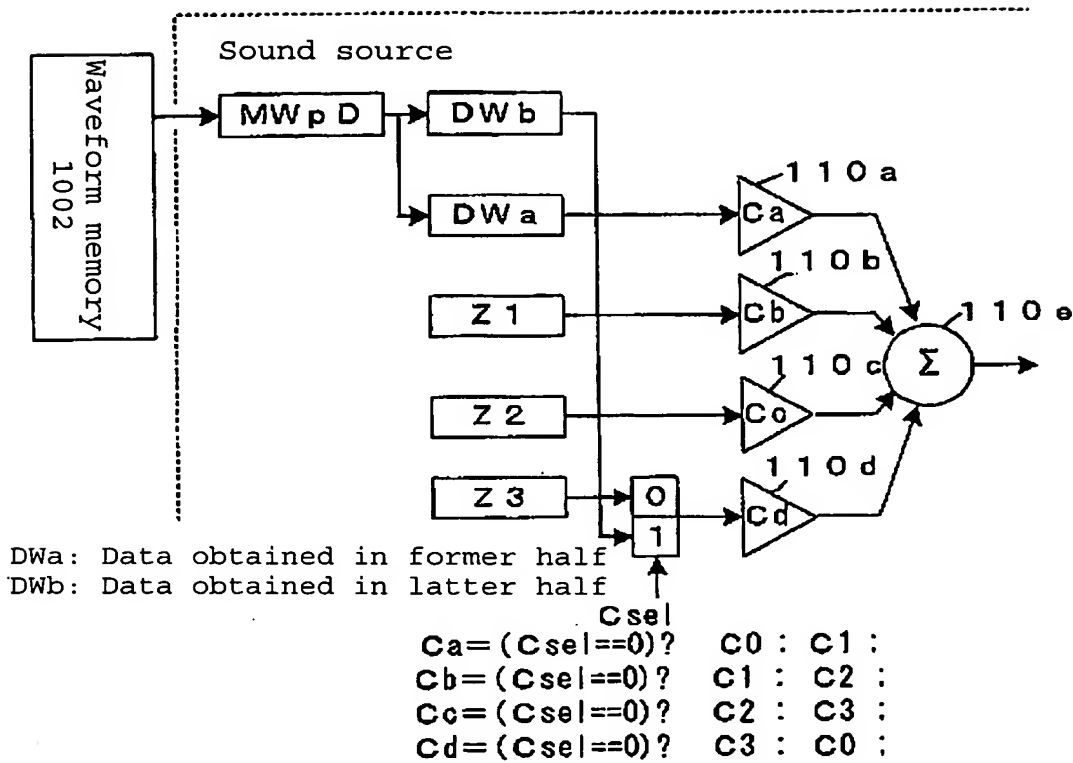


Fig. 6



Whenever one sample proceeds, data are renewed in the direction of Z3←Z2←Z1←DWa

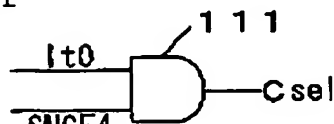
F i g. 7



F i g. 8

LSB indicative of
address in
waveform memory

Mode switching
signal



| lt0 | SNGF4 | Csel |
|-----|-------|------|
| 0 | 0 | 0 |
| 0 | 1 | 0 |
| 1 | 0 | 0 |
| 1 | 1 | 1 |

Whenever two samples
proceed, data are renewed
as in the form of
 $Z3 \leftarrow Z1$, $Z2 \leftarrow DWa$ and $Z1 \leftarrow DWb$

Fig. 9

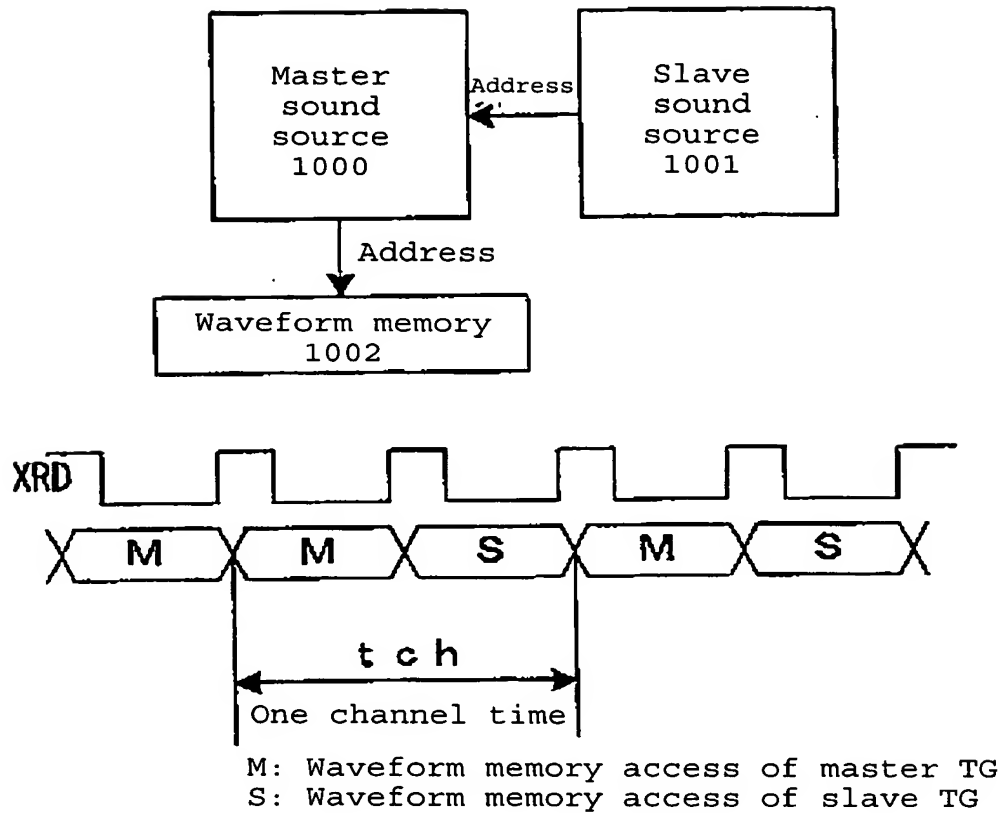


Fig. 10

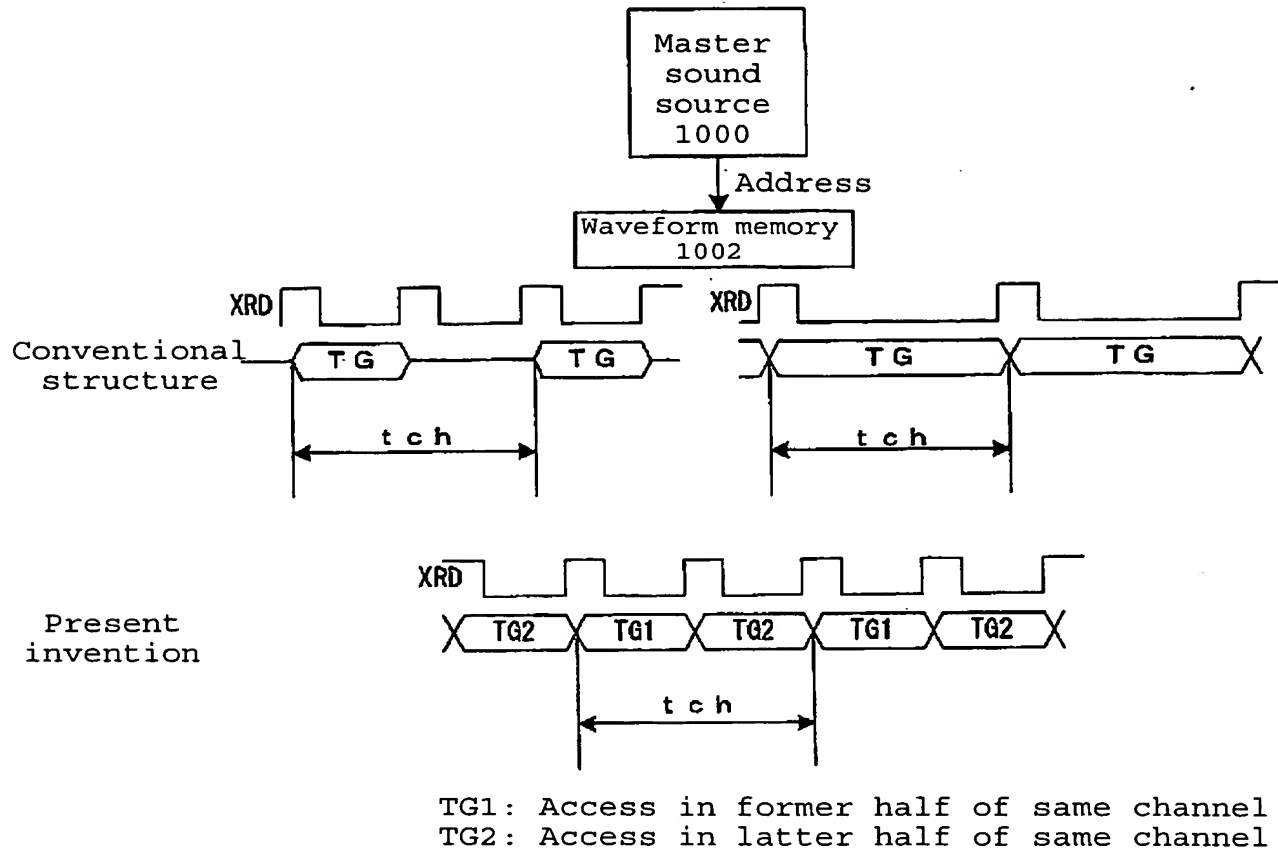


Fig. 11

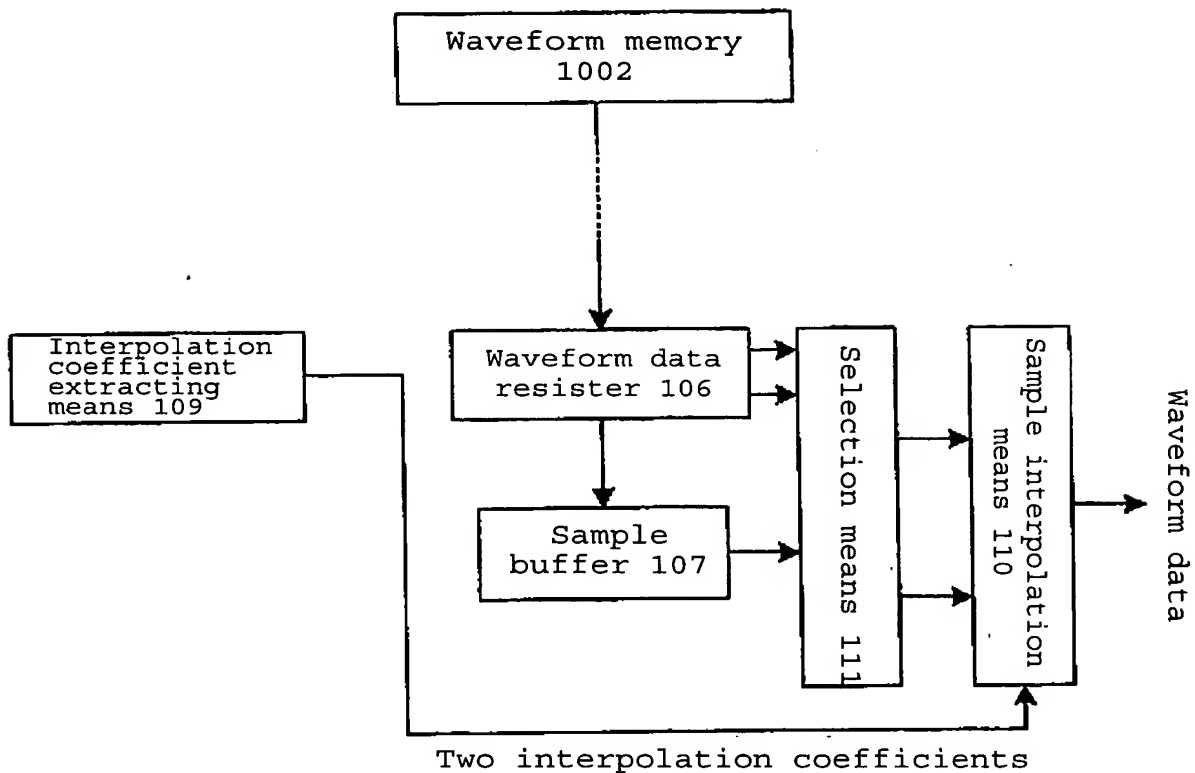


Fig. 12

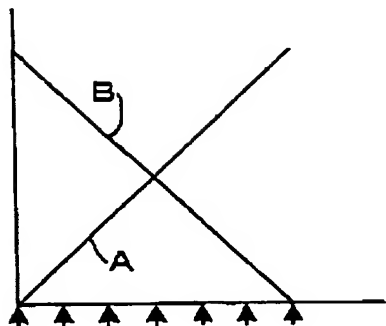


Fig. 13

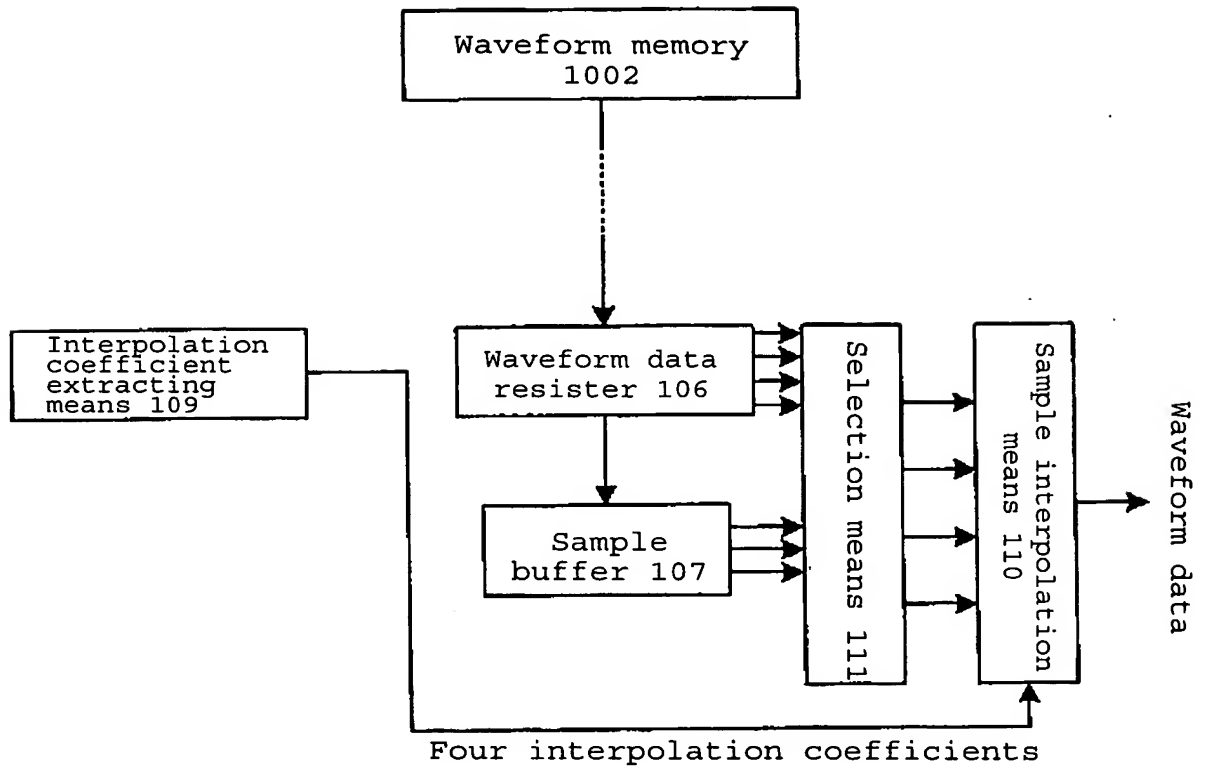


Fig. 14

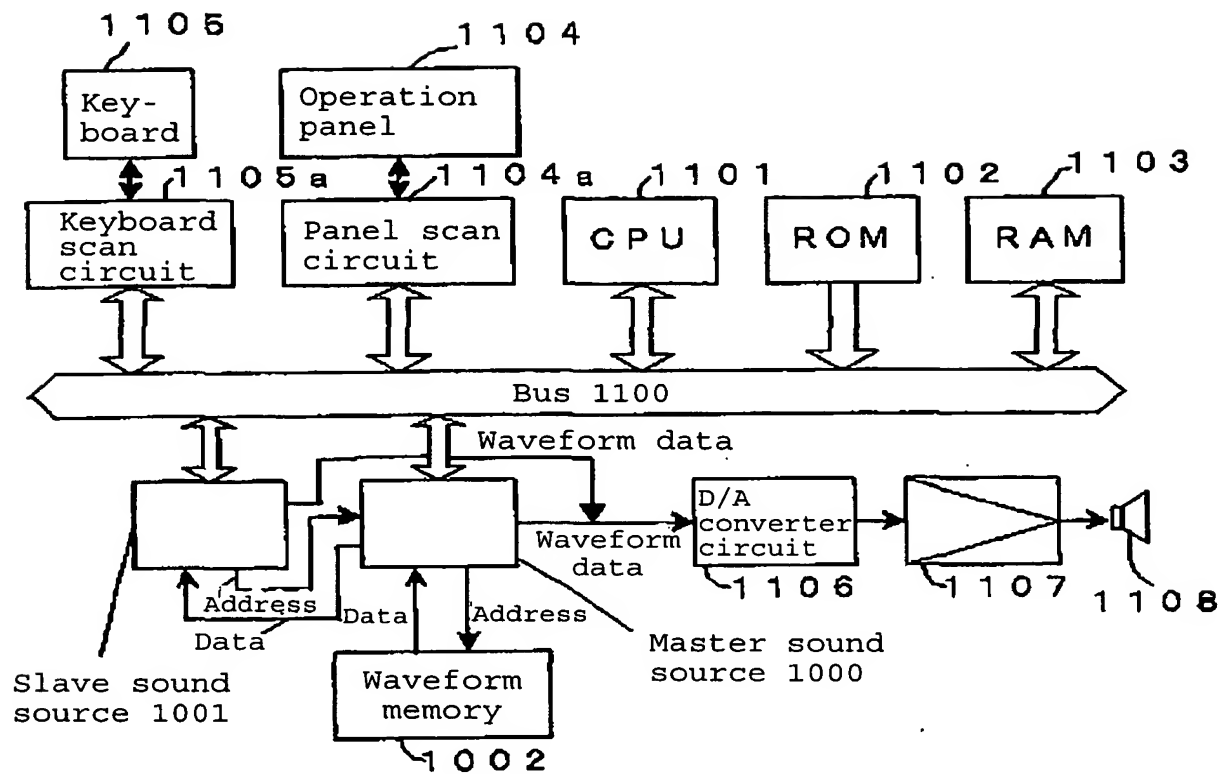


Fig. 15

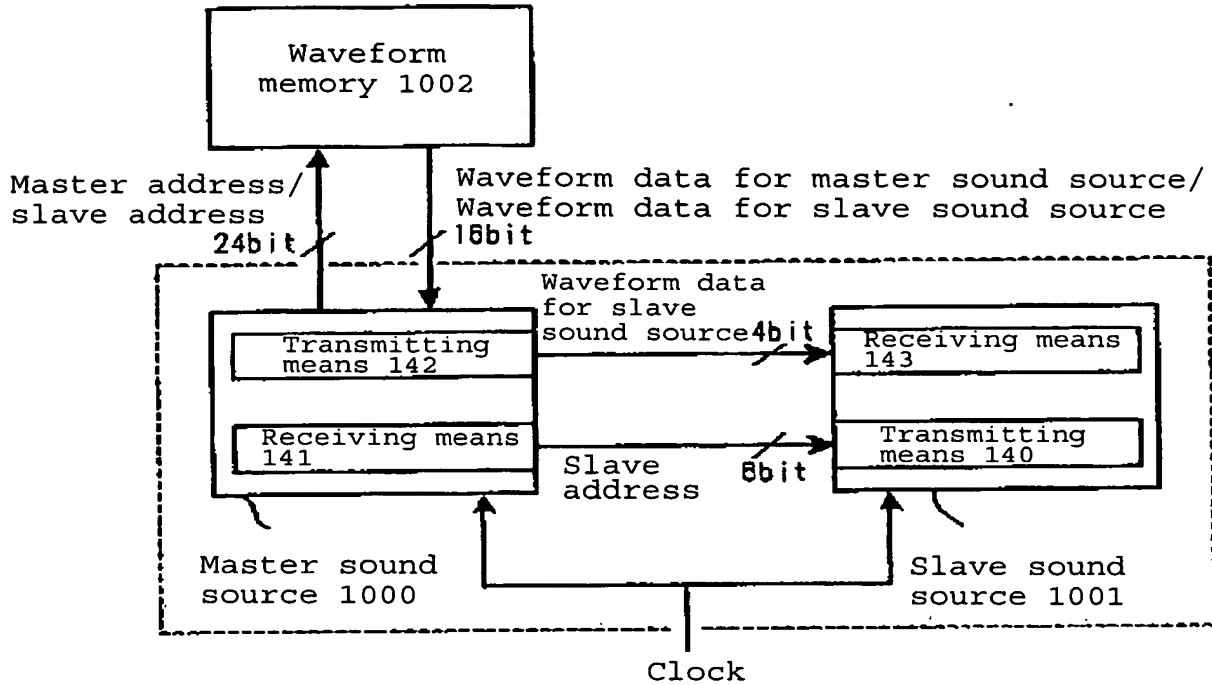


Fig. 16

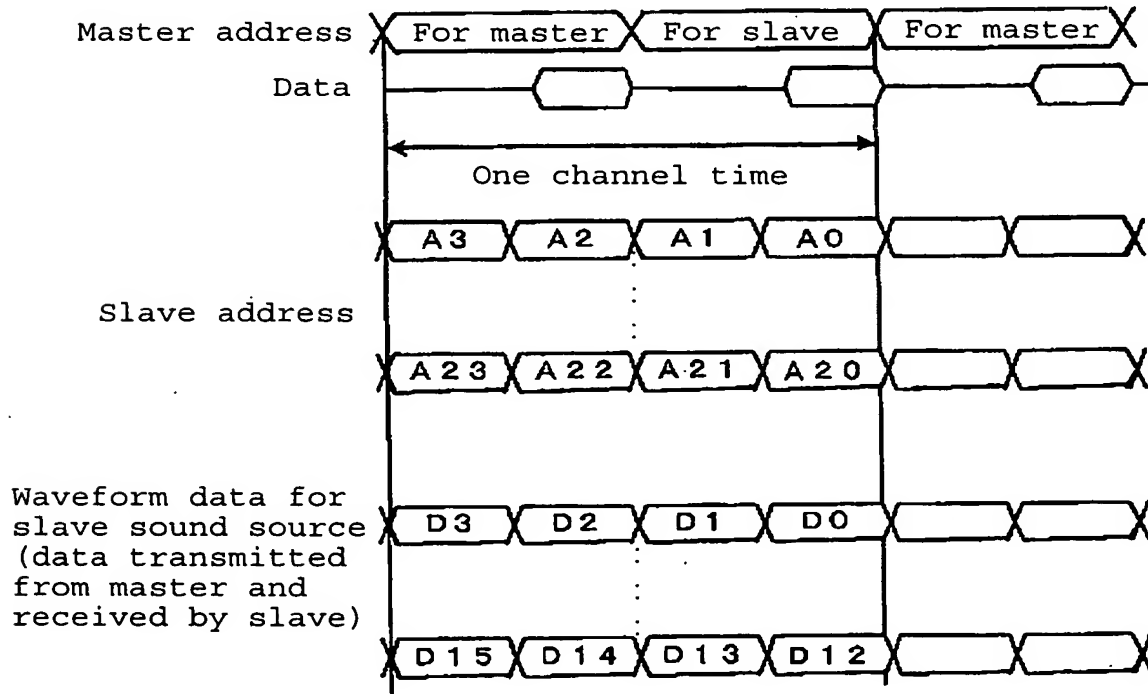


Fig. 17

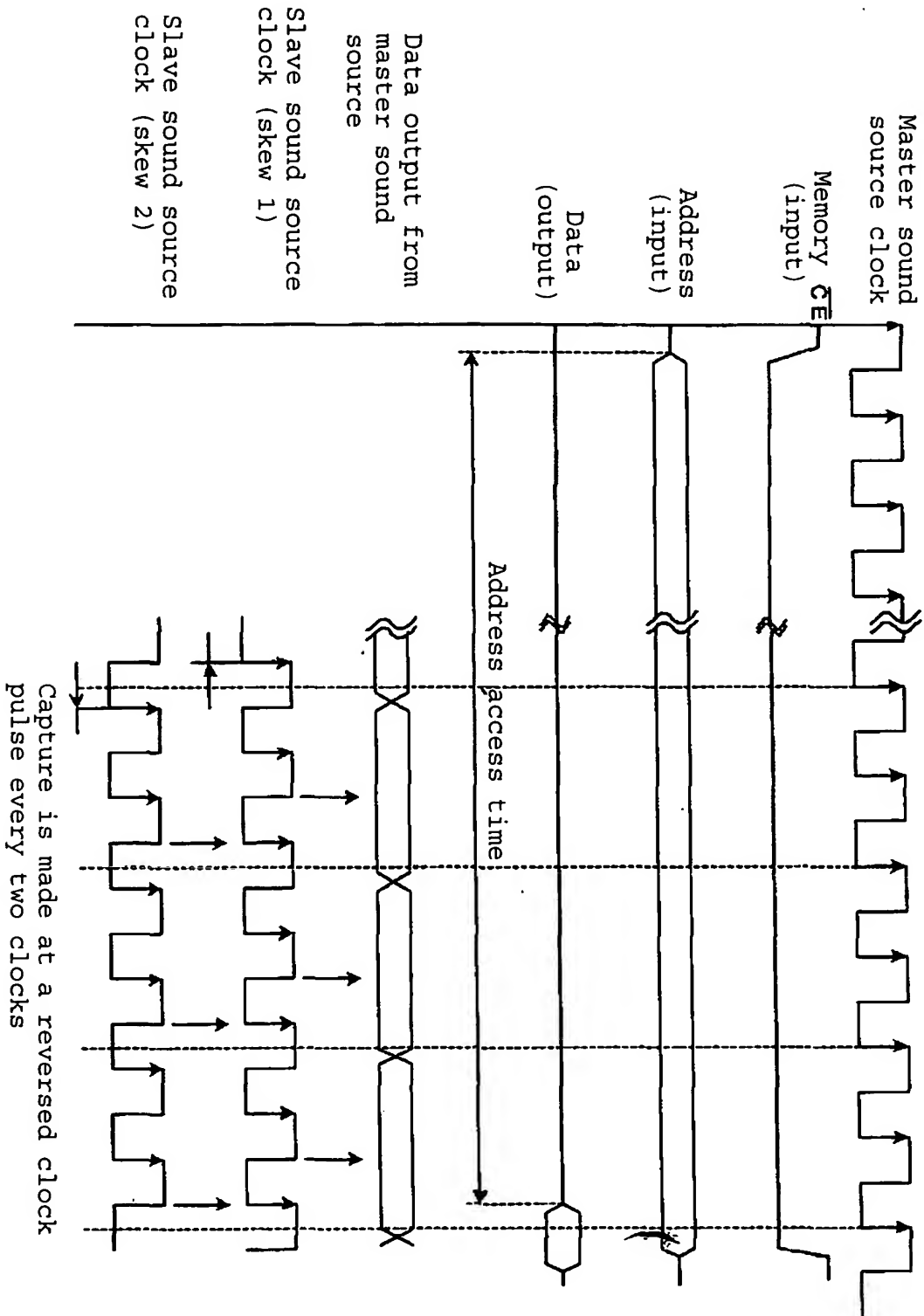


Fig. 18

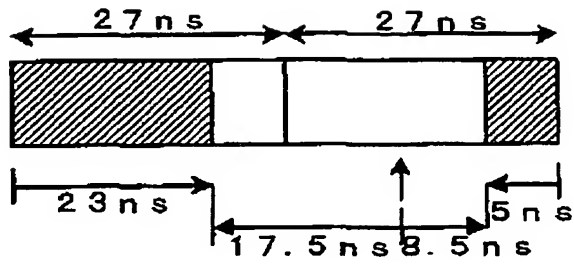
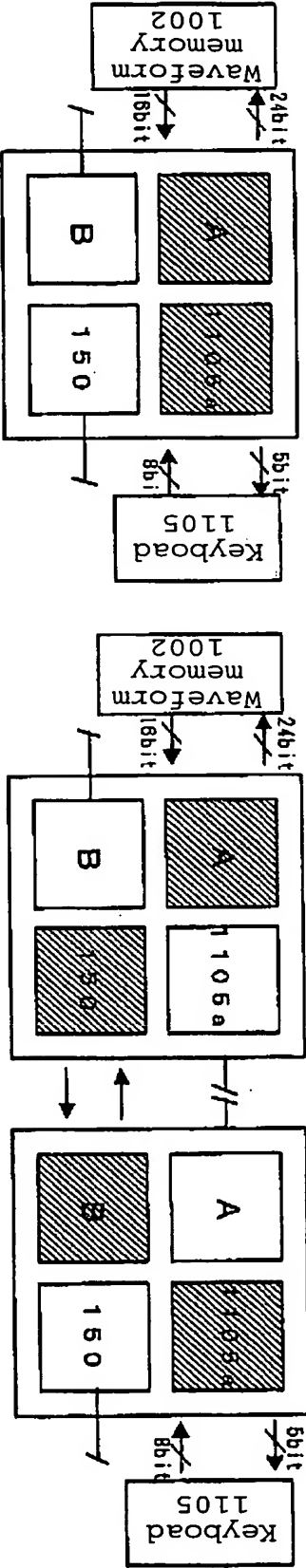


Fig. 19



| Functions of circuit blocks and the like | | Number of | |
|--|--|------------------|-----------------|
| | | output terminals | input terminals |
| A: External memory access circuit | | 24 | 16 |
| B: Address-output/data-input for slave sound source | | 7 | 4 |
| 1105a: Keyboard scan circuit | | 5 | 8 |
| 150: Data-output/address-input for master sound source | | 4 | 7 |

Fig. 20

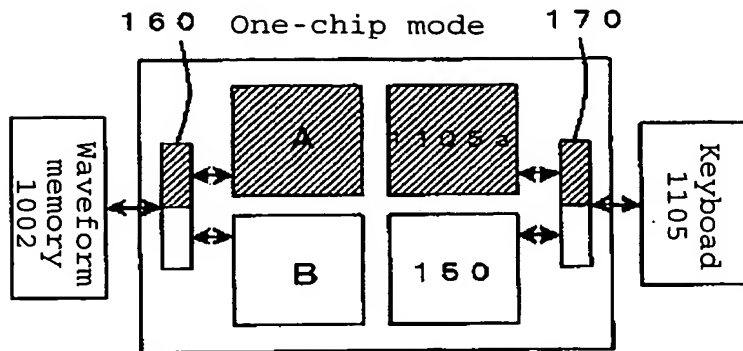


Fig. 21

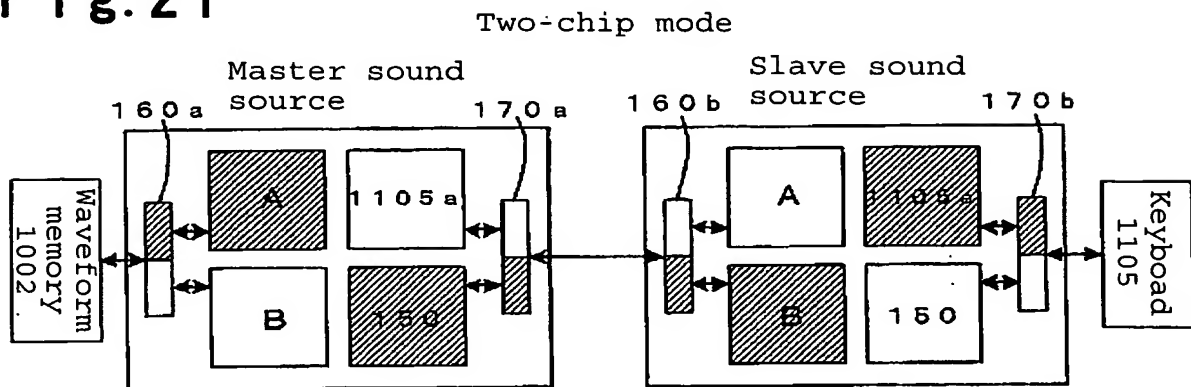
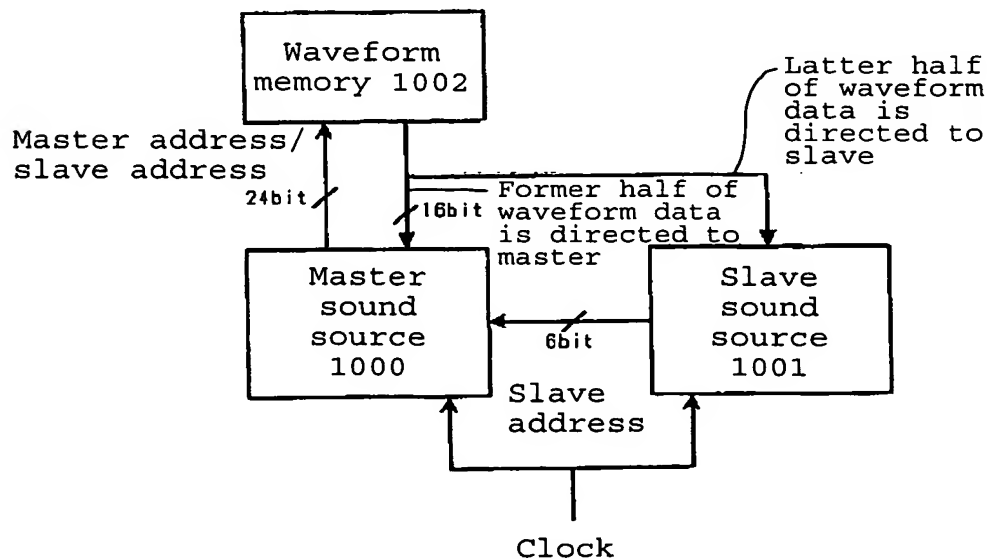
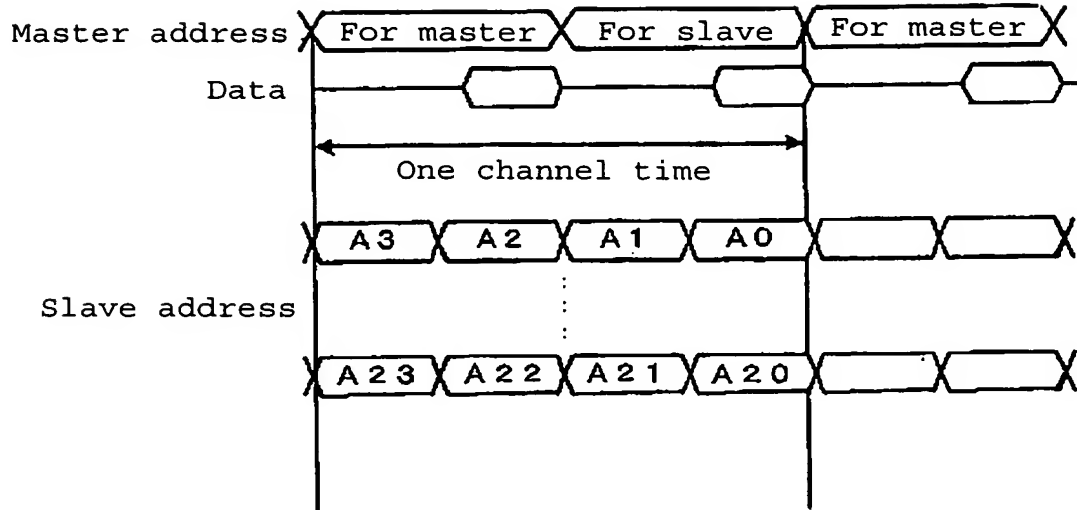


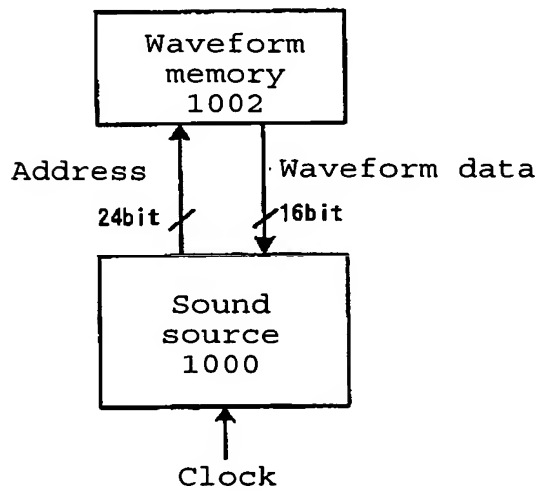
Fig. 22



F i g . 2 3



F i g . 2 4



F i g . 2 5

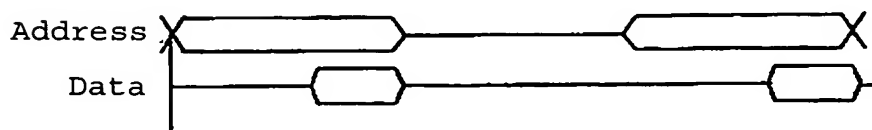


Fig. 26

